



**Foundation University Islamabad**  
**School of Science and Technology**  
**DEPARTMENT OF ELECTRICAL & COMPUTER ENGINEERING**  
**NOTIFICATION**

Dated: February 18, 2026

No.FUSST/Exam/BSEE,3/CE

Result notification of 'FA24-BSEE' , (Fall 2024 Batch) , 3 Semester , Fall 2025 Examination is hereby notified.

Sr.#	Registration #	Name	Course Title (Cr)	Course Title (Cr)	Course Title (Cr)	Course Title (Cr)	Course Title (Cr)	Course Title (Cr)	Course Title (Cr)	Course Title (Cr)	SGPA	CGPA	Results	Remarks
			LG	LG	LG	LG	LG	LG	LG	LG				
1	FUI/FUSST/F-24-BSEE-001	ABDULLAH	Linear Circuit Analysis(Imp)(3)	Programming Fundamentals(R)(2)	Programming Fundamentals Lab(Imp)(1)	Functional English(R)(3)	Calculus & Analytical Geometry(Imp)(3)	Applied Physics(Imp)(3)	--	--	2	1.93	Placed on 3rd Probation	Ceased to be Student of the University
			D	C	C+	D+	B	D						
2	FUI/FUSST/F-24-BSEE-002	IMAN FATIMA	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	3.7	3.71	Promoted	....
			B+	A	B+	A+	A+	A+	A+	A+				
3	FUI/FUSST/F-24-BSEE-006	MALAIKA NAEEM	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	2.6	2.72	Promoted	....
			C	B+	D	B	A	B	C	B+				
4	FUI/FUSST/F-24-BSEE-007	MEHAK FATIMA JAVEED	Programming Fundamentals(R)(2)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	--	2.8	2.47	Promoted	....
			C+	D+	B	C+	A+	B+	B+					
5	FUI/FUSST/F-24-BSEE-009	MUHAMMAD DANİYAL ARSHAD	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	2.3	2.54	Promoted	....
			D	C+	D	B+	C	C+	C	A				
6	FUI/FUSST/F-24-BSEE-010	MUHAMMAD KAIF	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	3.4	3.45	Promoted	....
			B	B+	B	A+	B+	A+	A	A+				
7	FUI/FUSST/F-24-BSEE-012	NOSHA SAQLAIN	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	2.6	2.81	Promoted	....
			D+	C+	D+	B	C+	A	C+	A+				
8	FUI/FUSST/F-24-BSEE-013	REHAN SHAHZAD	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	2.6	2.49	Promoted	....
			D+	A	D	B	B	B	C+	A+				
9	FUI/FUSST/F-24-BSEE-015	SAIF ULLAH	Data Structures & Algorithms(3)	Data Structures & Algorithms Lab(1)	Electrical Network Analysis(3)	Electrical Network Analysis Lab(1)	Digital Logic Design(3)	Digital Logic Design Lab(1)	Professional Ethics(3)	Complex Variable & Transforms(3)	2.6	2.55	Promoted	....
			D	B+	D+	B	C+	A	C+	A				



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			LG	LG	LG	LG	LG	LG	LG	LG				
10	FUI/FUSST/F-24-BSEE-016	SYEDA AMNA NASIR	<b>Programming Fundamentals(R)(2)</b>	<b>Electrical Network Analysis(3)</b>	<b>Electrical Network Analysis Lab(1)</b>	<b>Digital Logic Design(3)</b>	<b>Digital Logic Design Lab(1)</b>	<b>Professional Ethics(3)</b>	<b>Complex Variable &amp; Transforms(3)</b>	--	2.5	2.41	Promoted	....
			C+	D	B	C	A	C+	B+					

**Distribution**

1. MSA, FUSST
2. HoD, Concerned
3. Notice Board
4. Notification File

**Note** This notification is issued errors and omissions expected, as a notice only. An entry appearing in it does not in itself confer any right or privilege independently to the grant of a proper Certificate/Degree/Diploma which will be issued in due course.

Deputy Controller of Examinations

**Legend:**

- |   |  |  |
|---|--|--|
| 1. CSC2003 --Data Structures & Algorithms     | 2. CSC2003L --Data Structures & Algorithms Lab | 3. EEE1002 --Linear Circuit Analysis     |
| 4. EEE1121 --Programming Fundamentals         | 5. EEE1121L --Programming Fundamentals Lab     | 6. EEE2006 --Electrical Network Analysis |
| 7. EEE2006L --Electrical Network Analysis Lab | 8. EEE2031 --Digital Logic Design              | 9. EEE2031L --Digital Logic Design Lab   |
| 10. GER1200 --Functional English              | 11. GER1304 --Calculus & Analytical Geometry   | 12. GER2600 --Applied Physics            |
| 13. GER2715 --Professional Ethics             | 14. MTS2004 --Complex Variable & Transforms    |  |